PICJUG - Bug #443

can't get down to 1.8V due to frequency limit

10/01/2013 09:28 PM - Andrew Kohlsmith

Status:	New	**Start date:**	10/01/2013
Priority:	Normal	**Due date:**	
Assignee:		**% Done:**	0%
Category:		**Estimated time:**	0:00 hour
Target version:			

Description

The preliminary DC specifications state in parameter D001 that 1.8V can only be reached with a clock frequency <= 20MHz. 1.8V might not be a reality without conversion circuitry.

History

#1 - 10/31/2013 12:30 PM - Andrew Kohlsmith

74LVC07 might be a good buffer/translator. it's open-collector which means pullup resistors and possible power consumption/frequency limit issues.