PICJUG - Task #458

Schematic/layout updates for v1.1

10/31/2013 01:28 PM - Andrew Kohlsmith

Status:	New	**Start date:**	10/31/2013
Priority:	Normal	**Due date:**	
Assignee:		**% Done:**	0%
Category:		**Estimated time:**	0:00 hour
Target version:			

Description

toplevel issue to capture the schematic/layout changes needed for v1.1.

Subtasks:

Task # 459: FETs need to be swapped	**New**
Task # 460: Add level translator and run the PIC at VUSB (+5V) all the time	**New**
Task # 461: Remove the AP7115	**New**