PICJUG - Task #459

Task # 458 (New): Schematic/layout updates for v1.1

FETs need to be swapped

10/31/2013 01:29 PM - Andrew Kohlsmith

Status:	New	**Start date:**	10/31/2013
Priority:	Normal	**Due date:**	
Assignee:		**% Done:**	0%
Category:		**Estimated time:**	0:00 hour
Target version:			

Description

the parasitic diode is currently oriented so that it allows current flow even though the FET itself is off. Swap the drain and source.