iPod Nano 6 Display Breakout - Bug #600

Internal short between LED+ and RST#

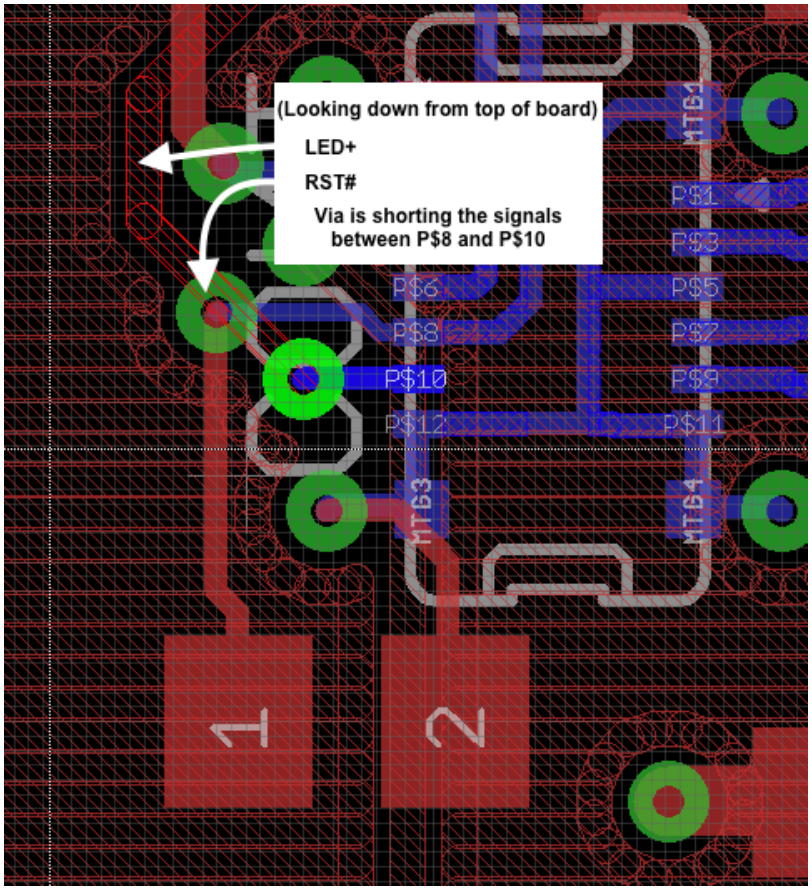
03/18/2015 04:44 PM - Andrew Kohlsmith

Status:	Feedback	**Start date:**	03/18/2015
Priority:	Normal	**Due date:**	
Assignee:	Andrew Kohlsmith	**% Done:**	80%
Category:		**Estimated time:**	0:00 hour
Target version:			

Description

I guess I didn't do the DRC with all layers on and ignored Eagle's "2 errors on hidden layers" warning.

The LED+ and RST# signals are shorted on layer 15 right at the LCD connector, and a via is too close to the LED+ trace on the same layer.



History

#1 - 03/18/2015 04:44 PM - Andrew Kohlsmith

- Status changed from New to Resolved

- % Done changed from 0 to 100

Resolved in commit:84af1ee.

#2 - 03/18/2015 04:45 PM - Andrew Kohlsmith

- Status changed from Resolved to Feedback

- % Done changed from 100 to 80

#3 - 03/18/2015 05:01 PM - Andrew Kohlsmith

- File picture8-1.png added

- Description updated

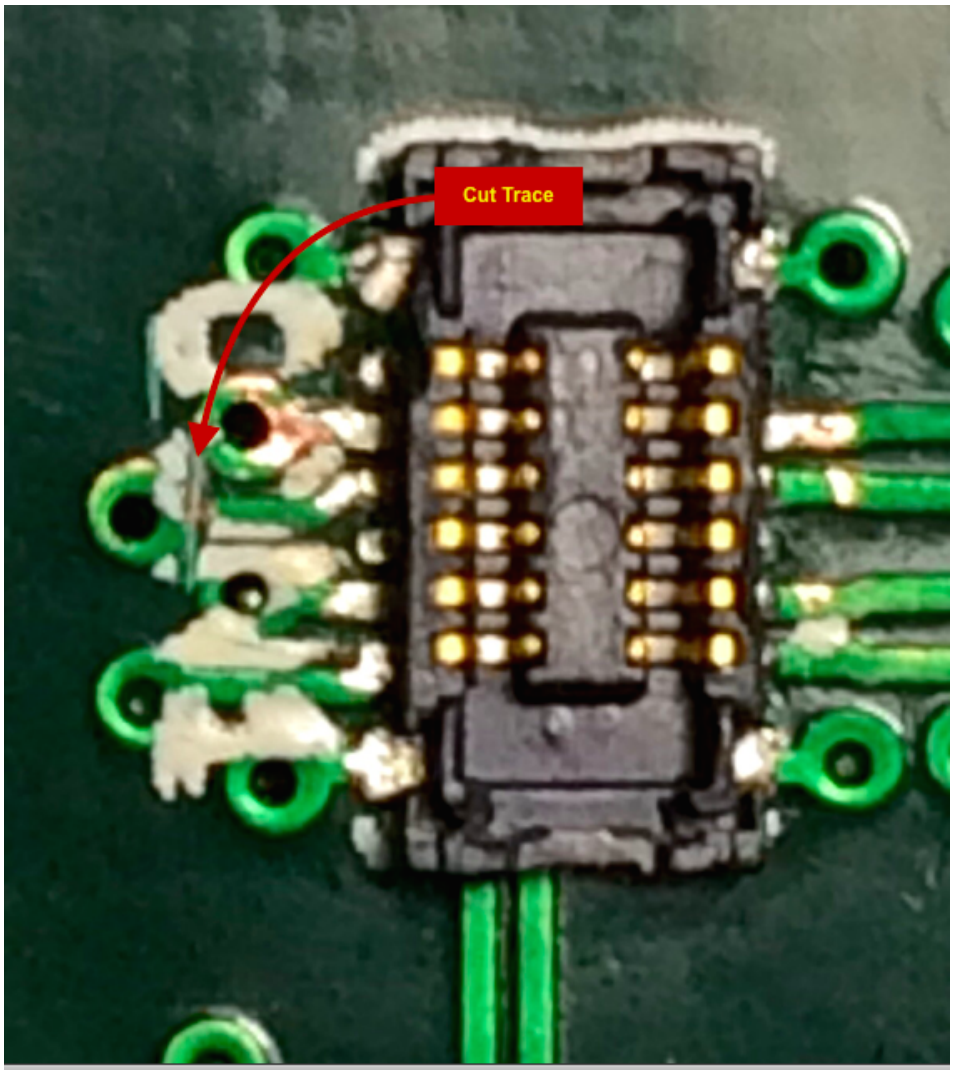
#4 - 03/18/2015 05:42 PM - Andrew Kohlsmith

- File picture964-1.png added

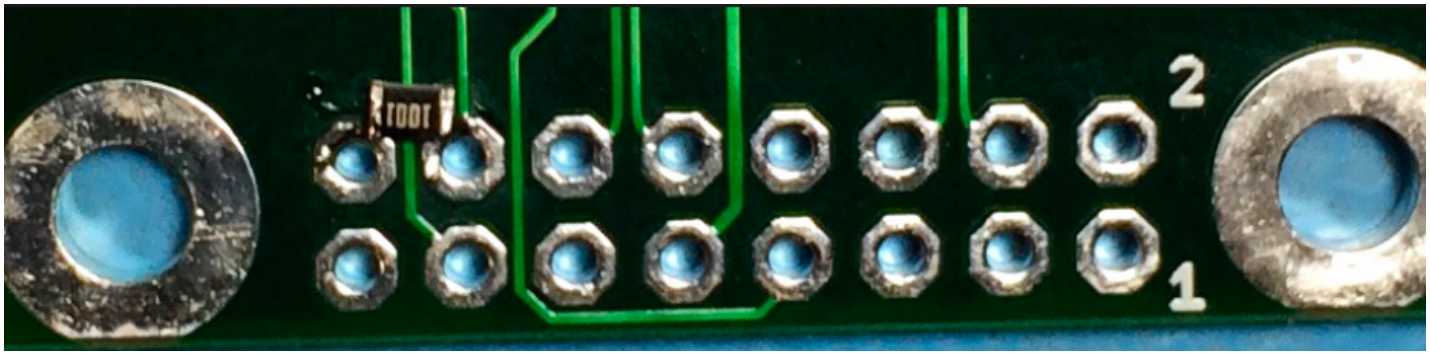
- File picture964-2.png added

The fix is easier than I suspected.

1. cut the trace between P$8 and the via that is shorting RST# to LED+



2. Install 1k pulldown resistor between pin 14 (RST#) and 16 (GND) of J1



3. Do not populate pulldown resistor R3

That's it!

Files

picture8-1.png	73 KB	03/18/2015	Andrew Kohlsmith

picture964-1.png	312 KB	03/18/2015	Andrew Kohlsmith
picture964-2.png	448 KB	03/18/2015	Andrew Kohlsmith